Photonic crystals (PhCs) are a class of artificial optical materials with periodic dielectric structures, which result in unusual optical properties. PhCs now show promise to be a key platform for future optical integrated circuits. Due to the unique properties of PhCs, the size of many optical components is anticipated to be greatly reduced by employing PhC structures, such as photonic crystal waveguides. In the most commonly employed configuration, a photonic crystal waveguide is formed by introducing a line defect into a two-dimensional (2D) PhC slab. In such PhC waveguides, light is confined by a combination of in-plane PBG confinement and vertical index guiding. A size reduction mechanism based on slow group velocity in photonic crystal waveguides has been discussed for an array of optical devices. Notomi et al. firstly demonstrated low group velocity and high group velocity dispersion using silicon PhC slab line defect waveguides. Several other groups also demonstrated this effect in both line-defect and coupled-cavity PhC waveguides.

In the context of microelectronics, silicon has been the optimal material for microelectronics for a long time, but it has only relatively recently been considered as an option for photonics. Silicon is transparent in the range of optical telecommunication wavelengths, 1.3 µm and 1.55 µm, and has high refractive index that allows for the fabrication of high-index-contrast nano-photonic structures. In addition, as silicon photonics technology is compatible with conventional complementary metal-oxide-semiconductor (CMOS) processing, monolithic integration of silicon photonic devices with advanced electronics on a single silicon substrate becomes possible. Optical modulators are pivotal components in silicon based optoelectronic integrated circuits. Most silicon electro-optic modulators are based on plasma dispersion effect, through which carrier concentration perturbation results in refractive index change. There are a number of ways to vary the carrier concentration in silicon including carrier injection and capacitive coupling though the metal-oxide-semiconductor (MOS) field effect. For broadband optical intensity modulators, the silicon Mach-Zehnder Interferometer (MZI) structure that converts a phase modulation into an intensity modulation is widely used. However, conventional silicon MZI modulators are based on rib waveguides, which usually need one-half to several millimeters to achieve the required phase shift in MZI structures. The reason is that propagation constant perturbation, Δβ, is fairly low, thus requiring larger rib waveguide length, L, to achieve required phase shift, Δφ = Δβ × L.

An ultra-compact silicon electro-optic modulator was experimentally demonstrated based on silicon photonic crystal (PhC) waveguides for the first time to our knowledge. Modulation operation was demonstrated by carrier injection into an 80-µm-long silicon PhC waveguide of a Mach-Zehnder interferometer (MZI) structure. The π phase shift driving current, Iπ, across the active region is as low as 0.15 mA, which is equivalent to a Vπ of 7.5 mV when a 50 Ω impedance-matched structure is applied. The modulation depth is 92%.

80-micron interaction length silicon photonic crystal waveguide modulator

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FIG. 1. Dispersion relation of a guide mode of a photonic crystal waveguide


\[ \Delta \beta_{PC} = \frac{d \beta_{PC}}{d \omega} \Delta \omega, \] which grows significantly whenever the group velocity \( \frac{d \omega}{d \beta_{PC}} \) approaches zero, e.g. on the right-most segment of dispersion curve in Fig. 1. Such an extraordinary growth of \( \Delta \beta_{PC} \) directly leads to a significant enhancement of phase modulation efficiency because the phase change is related to the change of propagation constant and waveguide length \( L \) as \( \Delta \phi_{PC} = \Delta \beta_{PC} \times L \). One can easily enhance \( \Delta \beta_{PC} \) by more than 100 times using a photonic crystal waveguide. Therefore, a 100 times shorter PhC waveguide can produce the same phase change as a long conventional waveguide.

The short device length is a benign feature for many other device performance considerations. The optical modulator device has a short PhC waveguide of a few tens of microns in length, which promises a low propagation loss. The power dissipation of the modulator is also expected to be one to two orders of magnitude lower owing to the much shorter electrode length.

A schematic of a silicon MZI modulator is shown in Fig. 2. The MZI modulator is composed of PhC waveguides, rib waveguides, Y-junctions, electrodes, and electrode pads. PhC waveguides are used in both arms of the MZI modulator to ensure the two arms have the same optical loss and dispersion; otherwise the modulation depth may suffer a reduction.

PhC waveguides of the MZI modulator are designed, fabricated and characterized \(^{23}\). A line-defect (W-1) PhC waveguide can be easily generated by removing a single row of air holes from a 2-D PhC slab. The dispersion diagram of PhC waveguides is calculated using the 3-D fully vectorial plane-wave expansion (PWE) method \(^{24}\). The slab is fabricated on a silicon-on-insulator (SOI) wafer. The thickness of the silicon core layer is \( t = 215 \text{ nm} \). The top cladding is air and the bottom cladding is a buried oxide layer of 2 \( \mu \text{m} \) thick. The pitch size of the hexagonal PhC lattice is \( a = 400 \text{ nm} \). The normalized air hole diameter is designed to be \( d/a = 0.53 \).

To fabricate the ultra-compact silicon MZI modulator, the designed PhC waveguides, rib waveguides, and Y-junctions are first fabricated on a SOI wafer. A PIP structure is formed by an implantation of boron at 25 keV with a peak concentration of \( 2 \times 10^{17} \text{ cm}^{-3} \) into an N-type Si substrate with the doping concentration of \( 1 \times 10^{14} \text{ cm}^{-3} \). Medici simulation tool shows the P-I-P diode injects holes only. The PIP I-V curve is experimentally confirmed for both forward and reverse biases. Note that the N-type Si substrate with \( 1 \times 10^{14} \text{ cm}^{-3} \) doping concentration is defined as intrinsic \(^{25}\). The PhC and Si rib waveguide structures are patterned with E-beam resist ZEP-520A by E-beam lithography (Jeol JBX6000). After developing the resist, the patterns are transferred to a 57 nm oxide mask layer by reactive ion etching (RIE) using CHF\(_3\). Then the E-beam resist residue is removed by plasma ashing in oxygen. Using the oxide layer as a hard mask, the patterns are transferred to the silicon core layer by a HBr and Cl\(_2\) RIE process. Post-etching oxidation at 850°C is implemented for about 1 minute. The post-etching oxidation forms an additional 5–7 nm oxide layer, resulting in the sidewalls of the air-holes being significantly smoother than the original surface after dry etching \(^{23}\). Extensive experimentation with various processes is conducted to determine the optimized process parameters. A proper pre-offset of the hole size in e-beam pattern design is used so that the hole size can be controlled with an accuracy of 5%. After the silicon photonic crystal waveguides and rib waveguides are fabricated, the regions for the aluminum electrodes and pads are patterned by a conventional photolithography mask aligner, followed by metal deposition and metal liftoff. Aluminum electrodes and pads are then sintered to form ohmic contacts with the top silicon layer. The SEM picture of the final structure is shown in Fig. 3, with the corresponding sections marked in Fig. 2.

FIG. 2. Schematic diagram of the silicon Mach-Zehnder PhC modulator. Electrode structure of the modulator with PIP regions indicated.

FIG. 3. SEM pictures of the silicon PhC modulator: (a) overview picture of the modulator. (b) PhC waveguide with two electrodes. (c) Y-junction. (d) Magnified PhC waveguide based on a triangular lattice with lattice constant \( a = 400 \text{ nm} \), hole diameter \( d = 210 \text{ nm} \), and top Si thickness \( t = 215 \text{ nm} \), buried oxide (BOX) SiO\(_2\) thickness of 2 \( \mu \text{m} \).
The modulated signal is displayed in Fig. 4(b) with sinusoidal wave. The modulation depth of 92% is clearly seen in Fig. 4(a). The optical output intensity against drive current is shown in Fig. 4(c). We have measured the transmission spectra which confirm that the silicon MZI modulator operating at 1567 nm. We have fabricated modulators. We characterize the modulation depth and the minimum current needed for phase shift of \( \pi \), of our silicon MZI modulator operating at 1567 nm. We have measured the transmission spectra which confirm that 1567 nm falls into the bandedge of the transmission spectra. The optical output intensity against drive current is shown in Fig. 4(a). The modulation depth of 92% is clearly seen in Fig. 4(a). The modulated signal is displayed in Fig. 4(b) with sinusoidal input signal at 300 kHz. The \( \pi \) radian drive current, \( I_\pi \), is a typical measure of the quality of such MZI modulator devices. The \( I_\pi \) of our silicon MZI modulator is as low as 0.15 mA compared to several mA in conventional MZI modulator devices, which shows the high quality of our MZI modulator device. With a 50 Ω impedance matched lumped electrode structure, it is equivalent to a \( V_\pi \) of 7.5 mV. The length of the modulator is reduced to 80 μm compared to several millimeters for the modulators using silicon rib waveguides in MZI structures due to the extraordinary dispersion of the PhC waveguide. All of these prove the proposed advantages of using PhC waveguide instead of conventional rib waveguide mentioned above. The thermo-optic effect is excluded as a mechanism for phase shift, because the power dissipation \( P_\text{R} \) is very low, and the subsequent temperature rise of the waveguide only less than 0.3 °C.

In conclusion, we designed, fabricated, and characterized an ultra-compact silicon electro-optic modulator based on silicon photonic crystal waveguides with a hexagonal lattice of air holes. Modulation operation was demonstrated by carrier injection into an 80 µm-long silicon photonic crystal waveguide. The modulation depth is over 92%. The \( I_\pi \) is as low as 0.15 mA. Further improvement in device performance is expected by optimizing the electrode design and reducing the contact resistance.

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